

REMARKS/ARGUMENTS

Favorable reconsideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 1-22, 24, 25, 27, 28 and 30-33 are pending in the present application with claims 1, 5, 13, 28 and 31 having been amended by the present amendment.

In the outstanding Office Action, claims 1-22, 24, 25, 27, 28 and 30-33 were rejected under 35 U.S.C. § 112, first paragraph, which is respectfully traversed.

Regarding this rejection, the Office Action indicates the resent request includes information identifying a storage area wherein the requested data is stored and that the specification does not describe how this task is performed. The Office Action also indicates the specification does not describe or suggest that the TR and K values from the sending entity are communicated to the receiving entity. The following comments are provided to address this rejection.

It is respectfully noted the receiving end can determine information about the memory in the sending end. For example, as noted at page 5, lines 2-4, the actual buffer size can be transmitted to the receiving end through an open logical channel. Further, because the resend request is generated by the video decoder of the receiving end, the video decoder can determine when an error occurs (see page 13, lines 23-28). For example, assume the QDC/QAC values for blocks B1, B2 and B3 are successfully received (see Figure 3A for an

explanation of the memory buffer). Then, assume the video decoder in the receiving end is processing the QDC/QAC values for block B4 and determines an error exist in the variable-length code AC3, for example. Thus, because the decoder previously successfully decoded AC2 for block B4, the decoder knows the error occurs in the block B4 for AC3. As discussed above, the receiving end knows the structure of the memory on the sending side, and can thus determine the memory location for the specific variable-length code AC3 of block B4. That is, because the buffer is partitioned according to variable-length codes, it is possible to gain individual access to the variable-length codes. Thus, because the video decoder knows when an error occurs in the decoding of a variable-length code, the decoder can determine the specific variable-length code that has an error and using the knowledge about the memory in the sending side, the memory address of the error data can be determined.

In addition, as noted above, the memory is divided into blocks with each block including variable length codes (see Figure 3A, for example). Thus, the TR values correspond to a block number. For example, a TR=3 corresponds to block B2 in Figure 3A, TR-1 corresponds to block B1 etc. The K values represent an extension of each block. That is, the K value identifies the address indicating where the QDC-R/QAC-R (i.e., data to be retransmitted) is stored. Further, independent claims 1, 5, 13 and 28 have been amended to clarify the memory structure. It is respectfully submitted one skilled in the art can make or

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use the invention based on the description in the specification and Figures as described above.

CONCLUSION

In view of the foregoing amendments and remarks, it is respectfully submitted that the application is in condition for allowance. If the Examiner believes that any additional changes would place the application in better condition for allowance, the Examiner is invited to contact the undersigned attorney, **David A. Bilodeau**, at the telephone number listed below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,
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